

2024「中技社科技獎學金」 2024 CTCI Foundation Science and Technology Scholarship

境外生研究獎學金

Research Scholarship for International Graduate Students



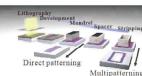
Self-expansion based multi-patterning for 2D materials fabrication beyond the lithographical limit

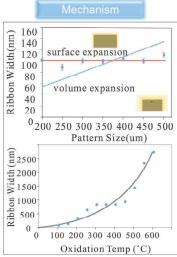
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Oxidative self-expansion double patterning (SEDP) process permit highly precise manipulation of the critical dimension in the nanometer-range, independent of the lithographic resolution. We demonstrate the potential of multi-patterning as a powerful platform for studying the integration of 2D materials into state-of-the-art, ultra-scaled devices.









Subsequent dry etching yields nanometer graphene patterns independent of lithographical resolution

of Aluminum achieves contact expansion

SEDP approach provides a route towards fabrication of 2D ultrascaled transistors for future electronics. Our approach is based on the combination of self-aligned process and lithographical patterning with high design flexibility and retention of 2D material quality. Approach is universally applicable to all 2D materials. It permits research into 2D material optimization and integration.

Selected Journal Publications

- 1. Self-Expansion Based Multi-Patterning for 2D Materials Fabrication beyond the Lithographical Limit. Small (2023): 2311209.
- 2. Transferrable Alumina Membranes as High-Performance Dielectric for Flexible 2D materials Devices. Advanced Electronic Materials (2024):





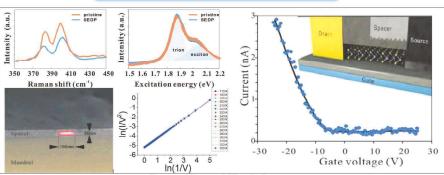


10nm Current (nA) 10 100 1000 Mobility (cm²/Vs -5 0 5 10 Gate Voltage (V)

· Raman spectroscopy reveals defectiveness independent of ribbon width and polarization dependence due to edges Selected area diffraction demonstrates the presence of two distinct lattices with a rotational misalignment of 12° TEM demonstrates low roughness independent of ribbon width **Electrical Measurements**

Characterizations

- Transconductance shows p-type behavior
- The average field effect mobility is 134cm²/Vs



Composition and Morphology

- Ultrathin 2D nanosheets are realized by self-expansion double patterning (SEDP) process
- Optical spectroscopy corroborates the retention of MoS₂ quality after processing.

Carrier Transport measurements

- Individual, 2D material nanosheets were integrated into FETs
- · Carrier transport occurs through direct tunneling
- Edge-contacted MoS₂ nanosheet transistor demonstrates semiconducting p-type behavior with a fieldeffect mobility of 0.52cm²/Vs

Awards and Recognition:

2024 - Unique Presentation Award in 15th Hope Meeting with Nobel Laureates

2024 - National Science and Technology Council (NSTC) Grant for Doctoral Student Attending International Conference Abroad

2023 - Institute of Atomic and Molecular Science Young Fellow Research Best Poster Presentation Award

2023 - IAMS - NTNU-YCU Autumn Workshop 2023 (INY 2023) Best Oral Presentation Award

2019 - Academia Sinica AS-Taiwan International Graduate Program (TIGP) Doctoral Fellowship

